

Uppgjord - Prepared UAB/B/TH Stefan Henriksson +46 8 727 4146		Datum - Date 2001-04-17	Rev B	Dokumentnr - Document no. 1301-ROF 366 808/1 Uen
Godkänd - Approved UAB/Y/DRC	Kontr - Checked			Tillhör/referens - File/reference FMIFV5R5_KGAjfaWDL

PRODUCT SPECIFICATION, PC board, ROF 366 808/1

1 **GENERAL**

1.1 **TECHNICAL INFORMATION**

ROF 366 808/1 is a PC board.

1.2 **INTERFACES**

cPCI

1.3 **POWER CONSUMPTION**

+3.3V
+5V
+/- 12V

1.4 **PRODUCT NUMBER(S)**

The following product versions has been specified:

ROF 366 808/1

1.5 **REPAIR**

All unit(s) delivered to Ericsson is/are bought as complete units and is/are tested by the supplier. The warranties stated in the contract are therefore valid only for the complete unit(s).

2 **R-STATE**

Table 1:

Ericsson Product number	Ericsson R-state	Purchased Company name	Purchased Product number	Purchased R-state
ROF 366 808/1	R1A	Teknor/Kontron	T-I 005_0EAA	0

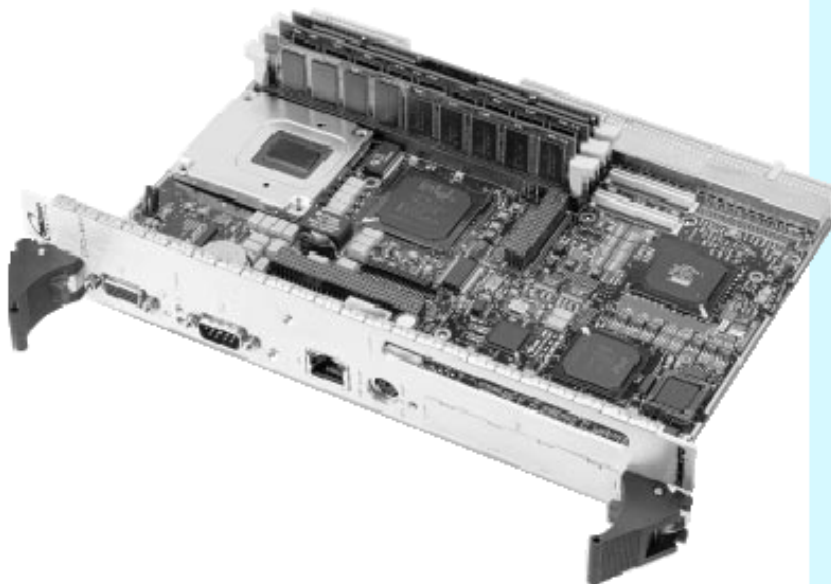
3 **REFERENCE(S)**

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4 INCLUDED PART(S)



with Pentium® II Processor - Low Power 333 MHz
or with Pentium® III Processor - Low Power 500 MHz



Low power, high availability

Teknor's 6U CompactPCI® peripheral processor, featuring scalable multiprocessing and the highest level of hot-swap capability, is the ideal peripheral processor for data/telecommunication, CTI, and industrial control/monitoring markets.

The cPCI-MXP 6U CompactPCI peripheral processor brings scalable multiprocessing capability to the Telecom and Industrial automation markets. It is designed to operate as a "slave" processor and can be installed in any CompactPCI peripheral slot where a standard system processor, such as Teknor's cPCI-MXS or cPCI-CXS, drives the system slot.

Fully hot-swappable, the cPCI-MXP communicates with other processor boards using the CompactPCI back plane as the physical layer of a 1 Gb/s Ethernet.

Teknor provides the necessary IP-Stacks to support local versions of various OSs including Microsoft Windows NT, Unix, VxWorks and pSOS. Using the cPCI-MXP's scalability, up to 15 tightly coupled x86 processors can be operated simultaneously in a single 19" CompactPCI system.

Multi-level PCI-to-PCI bridging is fully supported. Based on Intel's 21554 PCI-to-PCI non-transparent bridge, the feature set of the cPCI-MXP includes a rich selection of standard peripherals.

The cPCI-MXP offers a natural growth path to high performance, high availability as well as hot swap and scalable multiprocessing technology.

Teknor's cPCI-MXP with Pentium® III/III processor - Low Power-based peripheral processor provides performance, low power, hot-swap capability and scalable multiprocessing in a standard single or dual-slot CompactPCI® 6U form factor.

Features Include:

- Up to Pentium III processor - Low Power 500 MHz with on-die 256KB L2 Cache Intel 440BX AGPset
- Up to 768MB SDRAM
- PCI-to-PCI non-transparent bridge, multiprocessing using IP-stack
- PMC, USB, Serial and Parallel Ports, PCI EIDE
- Hot-Swappable
- 10/100Base-TX Ethernet and one AGP Video Controller

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cPCI-MXP - Technical Specifications

CPU

- Pentium II processor - Low Power 333 MHz or Pentium III processor - Low Power 500MHz
- Intel 440 BX AGPset
- Hot-swappable

Bus Interfaces

- Front side bus up to 100 MHz
- CompactPCI Bus, 32-bit (33MHz) J1 and J2
- PCI-to-PCI non-transparent bridge: Intel's 21554; supports up to 7 REQ/GNT for fully loaded CompactPCI system
- PCI mezzanine (PMC)
- Proprietary mezzanine with PCI bus, FD and EIDE support
- SM Bus (for system management of CPU temperature monitoring, DRAM control, Clock buffers and power control)

Cache

- 16/16 KB Instruction / Data CPU internal Level 1
- 256 KB 64-bit wide on-die Level 2 pipelined burst

Memory

- Three 168-pin latching DIMM sockets, 64/72-bit
- Up to 768 MB of SDRAM with parity or ECC (for single bit error correction and double bit error detection)

Data Path

- 64-bit on CPU and video memory; 32-bit on local PCI and CompactPCI bus

Interrupts

- 11 edge sensitive and configurable
- 4 PCI level sensitive, configurable to any interrupt vector for PnP compatibility

Flash Memory

- 512 KB for BIOS field upgrade; Silicon Serial ID TAG for unique board identification accessible via software

IO

IO: SMC FDC37C872

USB Ports: Two

Serial Ports: Two (one RS-232, COM1, COM2 configurable as RS-422/485)

Parallel Port: One bi-directional with all IEEE 1284 protocols supported with BIOS selectable IRQs and addressing

Floppy Disk: Support for two drives (360 KB to 1.44 MB)

EIDE: Two channel Bus Master PCI EIDE; support for four IDE Type 4 drives (master/slave configuration); LBA, PIO Mode 0-4 and Ultra DMA/33

CompactFlash™ Module: Optional bootable CompactFlash™ disk interfaces to primary EIDE channel, user upgradeable, master / slave

Ethernet: One 10/100 Mb/s Ethernet, PCI 10/100Base-TX ports (Intel 82559 controller) HD / FD Mezzanine Card: Optionally onboard using Teknor's cMC series mezzanine cards

Video

- 64-bit AGP video controller (Intel 69000) with 2 MB video memory
- CRT resolutions up to 1024 x 768 x 64K colors or 1280 x 1024 x 256 colors

Clock / Calendar

- Real-time clock with (replaceable) battery backup, CMOS RAM

Connectors in "Front" configuration

- CRT (15-pin D-sub); serial COM 1 (9-pin D-sub); two Ethernet (RJ-45 with link / activity indicators); PS/2 mouse / keyboard (6-pin mini-Din) interfaces on J3/J4/J5 (Rear-panel transition module, cTMB0-ST02S available separately)
- CRT; 2 x serial; 2 x USB; 1 x parallel; SM Bus; I2C; speaker VF; Reset; IR; 1 x Ethernet; PS/2 mouse; keyboard; SCSI; 2 x EIDE; floppy disk interface; fan fail monitoring

BIOS Features

- Award Elite BIOS in Boot Block Flash with recovery code; save CMOS in Flash option, and boot from LAN capability
- Auto configuration, extended setup;
- CC000-E0000 address blocking; PnP tables
- Setup console redirection to serial port (VT100 mode) with CMOS setup access
- Software enable/disable of onboard Ethernet; hardware enable/disable of onboard video
- Diskless, keyboardless, and videless operation extensions;
- System, video, and LAN BIOS shadowing
- Programmable bus and IO speeds, and memory wait states
- Advanced security feature for floppy and HDD; DM1 & HDD S.M.A.R.T. support
- Advanced Configuration and Power Interface (ACPI 1.0), Advanced Power Management (APM 1.2), advanced thermal management (resume, overheat alarm and auto slow down), and Green support

Supervisory

- Two-stage software programmable Watchdog timer drives NMI on 1st stage and system reset on 2nd stage
- Programmable CPU temperature monitor/alarm
- Power failure / low battery detector
- Front Panel LEDs: IDE activity, SCSI activity, Ethernet activity & link

OS Compatibility

- MS-DOS™, Windows® 95/98, Windows® NT, VxWorks™, pSOS™, QNX™, Linux, and Solaris Mechanical (compliant to IEEE 1101.10; compliant to PICMG 2.0 Rev 2.1)
- 233 x 160 x 41 mm / 9.2 x 6.3 x 1.6 in; 6U x BHP (dual slot) Mechanically
- 233 x 160 x 20.5 mm / 9.2 x 6.3 x 0.8 in; 6U x 4 HP (single slot) Mechanically

Power Requirements

Supply Voltages:	+3.3V:	±5% / +5V ±5% / +12V ±5%
Supply Current:	3.3V:	2.0 Amps max.
	5V:	5 Amps max.
	12V:	0.75 Amps max.

Power Dissipation: 40W max.

* Pentium II processor - Low Power 333 MHz with 32MB SDRAM

Environmental

	Operating	Storage and Transit
Temp.:	0-60°C / 32-140°F (w/airflow)	-40° to +70°C / -40° to 198°F
Humidity:	5% to 95% @ 40°C/104°F non-condensing	5% to 95% @ 40°C/104°F non-condensing
Altitude:	4,572 m / 15,000 ft	15,240 m / 50,000 ft
Shock:	Designed to meet IEC 68-2-27	
Vibration:	Designed to meet IEC 68-2-8	

Reliability

- MTBF: > 100,000 hours @ 55°C / 131°F (MIL-HDBK-217F)
- Board serial number in EEPROM
- USB, keyboard, mouse and SCSI voltage protected by self-resetting fuses
- 2 year limited warranty
- Designed to meet or exceed:
- Safety: UL 1950; CSA C22.2 No 950; EN 60950; IEC950
- EMV/EMC: FCC 47 CFR Part 15/CISPR22, Class B; CE Mark to EN 55022/EN 50082



Teknor Applicom helps customers speed to market with applied computing solutions featuring single board computers and integrated systems specifically designed for Industrial Automation, Internet, Telecommunications, Mobile Computing and all types of high-speed, high-availability applications.

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