

Prepared (also subject responsible if other) EAB/UY/IF Bengt Sundling		No. 2/1510-CXC 106 0046 Uen		
Approved UAB/Y/IUC (-)	Checked	Date 2002-09-16	Rev C	Reference PBOOT_RPI_860

MANUFACTURING INSTRUCTION: RPI BOARD PROGRAMMING

1 ABSTRACT

This instruction tells how to load the PBOOT onto an RPI board in production time.

2 PURPOSE

The intended user of the document is the manufacturer and the responsible for setting up a correct production programming environment. Intended reader of the document is the board manufacturer in general and the factory programming responsible in particular. It is not a complete instruction how to handle production programming, the intention is only to specify some important matters.

This document is released with the software listed in chapter 3

The software itself is stored in GASK together with this instruction.

3 APPLICATION

PBOOT_RPI_860

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SUMMARY

This document gives information about PBOOT programming for the RPI related to the product PBOOT_RPI_860, CXC 106 0046.

With PBOOT_RPI_860 also the product RPI_CORE, CXC 106 0044 is required.

The relationship between the revision of this document and the product revision of CXC 106 0046 is documented in 1095-CXC 106 0046.

If new CXC revision is released, this document might also be updated to reflect any change.

Chapter 4 is a high-level instruction for PBOOT. The detailed instruction is developed and maintained by the manufacturer.

Recommended documentation of each board (ROJ level) includes a dedicated manufacturing instruction to document the specific rules and circumstances that applies for this board. Document class is 2/1510-ROJ.

The document survey (1095-) of the board must include the PBOOT_RPI_860 and revision in order to reflect that this piece of software should be loaded.

In general there are (or will be) other RPI based boards that will not have exactly this PBOOT (PBOOT_RPI_860) as basic firmware configuration. This instruction only apply as long as the PBOOT_RPI_860 is specified.

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4.1 REVISION HISTORY

Rev	Date	Prepared by	Comment
A	2001-04-25	Johan Struwe	Released
B	2002-09-12	Bengt Sundling	Rev references removed in reference table
C	2002-09-16	Bengt Sundling	Updated for R3A. Revision referencies removed generally.

4.2 REFERENCES

Document	Title
1/155 01-CNZ 212 330 Uen	Implementation of firmware handling for RPI
2/1510-CXC 106 0044	Manufacturing instruction RPI_CORE

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5 HIGH-LEVEL INSTRUCTION

Note: This is only an outline to convey understanding of how the software is packaged and loaded onto target. The manufacturing process must elaborate this further, and this is beyond the scope of this document.

To apply PBOOT_RPI_860 on target board:

1. Clear FLASH area (fill with 0x00) with the size that corresponds to the area that will be used for firmware storage.

- Unused FLASH area at the end of the FLASH devices is NOT required to be cleared. This reduces the amount of time to prepare the firmware programming. The total FLASH area consists of 8 Mbyte.

2. Load Motorola S3-record of PBOOT_RPI_860 via JTAG interface. The file is named 19112-CXC1060046.

- The production process may require that the file is byte-swapped (big-endian/little-endian). However, this is programming tool-dependent and cannot be specified here.

3. Now continue with next step: that is apply RPI_CORE (see 2/1510-CXC106 0044)